Measurements and calculations of capacitances of BJT and SJT made of silicon carbide

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Abstract. In the paper the results of capacitance-voltage (C-V) characteristics measurements of the current controlled BJT and SJT transistors were presented, for which a programmable measuring system manufactured by Keithley was used. The results of measurements was compared with results of the calculations obtained by using the commonly used Gummel-Poon model. For comparison, the results of the measurements found in the literature and in the datasheets of the considered devices were presented as well. The temperature impact on the shape of the considered characteristics was also investigated.

1 Introduction

The rapid development of electronic technology makes silicon carbide (SiC) one of the most promising materials for producing high power semiconductor devices with relatively short switching times [1,2]. This group of devices includes current controlled transistors such as classic SiC BJT (Bipolar Junction Transistors) offered by TranSiC [3] and modern SJT (Silicon Carbide Junction Transistor) with a relatively high current gain factor, offered since 2011 by GeneSiC [4]. The paper presents the form of the Gummel-Poon model and an evaluation of the accuracy of this model in determining the capacitance-voltage characteristics of arbitrarily selected BJT and SJT made of silicon carbide. The results of the calculations were compared with the results of the measurements. The temperature impact on the shape of considered characteristics was also investigated.

2 Gummel-Poon model form

In practice, modelling characteristics of a current controlled transistors is most often realised by using the Gummel-Poon model [5, 6]. The popularity of this model is evidenced by the fact that the considered model is implemented in many well-known computer programs for analysis of electronic circuits such as SPICE software [7]. Moreover, manufacturers of semiconductor devices usually provide parameter values for the mentioned model in the devices datasheets. The capacitances \( C_{bc} \) and \( C_{be} \) are responsible for modelling the non-linear capacitances of the base-collector and the base-emitter transistor junctions. The considered capacitances are described as follows [7]:

\[
C_{bc} = C_{bc}^{\text{area}} \cdot \frac{XCJC}{C_{jbc}}
\]

(1)

\[
C_{be} = C_{be}^{\text{area}}
\]

(2)

A detailed description of the analytical dependencies of the Gummel-Poon model, is available for example in work [7].

3 Results of the measurements and calculations

The paper presents the results of C-V characteristics measurements of the arbitrarily chosen transistors: SiC BJT (BT1206AC) and SJT (2N7635-GA). The measurements were performed using the Keithley 4200-SCS programmable system [8] in a wide range of an ambient temperature. In the case of the SiC BJT, a set of parameter values, obtained during the estimation procedure [9,10], was used in the calculations while in the case of the SJT, the calculations were performed using a set of parameter values, taken from the device datasheet [4]. Figure 1 depicts the \( C_{bc}(V_{BC}) \) characteristics of the SiC BJT where a filled points represent the results of an author’s measurements, an unfilled points represent the results of measurements included in research paper [5] and a solid lines represent the results of calculations using Gummel-Poon model.

Fig.1. \( C_{bc}(V_{BC}) \) characteristics of the BT1206-AC transistor at the ambient temperature equal to 25°C.

As seen (Fig. 1), both the qualitative and quantitative agreement of the calculation results and measurements,
in the $v_{BC}$ voltage range below 1.8 V, was obtained, which proves the high accuracy of the Gummel-Poon model in this operation range. Figure 2 presents the temperature impact on the $C_{iss}(v_{BC})$ characteristics. It turns out that in case of the SiC BJT, the influence of an ambient temperature on the considered C-V characteristic is barely perceptible for the bias in the reverse direction.

Figure 3 shows the $C_{iss}(v_{SD})$ and $C_{rss}(v_{SD})$ characteristics of the considered SJT transistor at a fixed value of the base-emitter voltage equal to 0 V. A filled points represent the results of an author’s measurements, an unfilled points represent the results of measurements taken from device datasheet [4] and solid lines represent the results of calculations using Gummel-Poon model. The capacitances $C_{iss}$ and $C_{rss}$ are described as follows [4]:

$$C_{iss} = C_{bc} + C_{be}$$  
$$C_{rss} = C_{bc}$$

The slight temperature effect is visible on considered $C_{iss}/C_{rss}(v_{SD})$ characteristics presented on Figure 4. For example, with the increase of the ambient temperature by 150°C, a value of the $C_{rss}$ capacitance increases for only about 20 pF for the base-collector junction biased in reverse direction and for about 50 pF for the polarity in the direction of conduction.

**References**


